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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): Luk et al.
Docket No: YOR920030604US1
Serial No.: 10/751,713
Filing Date: January 5, 2004
Group: 2818
Examiner: Unassigned

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature:  Date: January 12, 2005

Title: 3TID Memory Cells Using Gated Diodes and Methods of Use Thereof

STATUS REQUEST

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

To date, we have received no communication or official action from the Examiner in charge of the above-identified patent application. Please let us know the status of this case.

Respectfully submitted,


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